

**Listing of Claims:**

Claims 1-2 (canceled)

Claim 3 (currently amended) ~~The method of claim 2, wherein the pyramid filter bank comprises~~ A method of implementing a two-dimensional pyramid filter bank and comprising:

first adding a first and a last input signal sample to a sum of input samples of a next lower-tap filter of a current filter to produce a sum of input signal samples for the current filter; and

second adding the sum of input signal samples for the current filter to an output signal sample of the next lower-tap filter of the current filter to produce an output signal sample for the current filter,  
wherein the first and second adding is performed by different adders and is applied by column and by row.

Claim 4 (original) The method of claim 3, wherein the column and the row adding is performed independently.

Claim 5 (canceled)

Claim 6 (currently amended) The method of claim 3 ~~[[5]]~~, wherein the first and second adding is performed progressively.

Claims 7-10 (canceled)

Claim 11 (currently amended) ~~[The article of claim 10, wherein the instructions, when executed, further result in the current filter comprising a]~~ An article comprising: a storage medium, said storage medium having stored thereon instructions, that, when executed result in:

first adding a first and a last input signal sample to a sum of input samples of a next lower-tap filter of a current two-dimensional pyramid filter bank to produce a sum of input signal samples for the current filter; and

second adding the sum of input signal samples for the current filter to an output signal sample of the next lower-tap filter of the current filter to produce an output signal sample for the current filter, the first and second adding being performed by different adders, and the first and second adding being applied by column and by row.

Claim 12 (original) The article of claim 11, wherein the instructions, when executed, further result in the column and the row adding being performed independently.

Claim 13 (canceled)

Claim 14 (currently amended) The article of claim 11 [[13]], wherein the instructions, when executed, further result in the first and second adding being performed progressively.

Claims 15-17 (canceled)

Claim 18 (currently amended) ~~The integrated circuit of claim 17~~ An integrated circuit comprising:  
digital logic circuit components coupled so that, during operation, a first and a last input signal sample are added to a sum of input samples of a next lower-tap filter of a current filter to produce a sum of input signal samples for the current filter and so that the sum of input signal samples for the current filter are added to an output signal sample of the next lower-tap filter of the current filter to produce an output signal sample for the current filter, wherein the digital logic components include a multiplexer, two flip-flops, a two-input adder and a three-input adder.

Claim 19 (currently amended) The integrated circuit of claim 18 [[17]], wherein, during operation, the current filter comprises a two-dimensional pyramid filter bank and the adding is applied by column and by row.

Claim 20 (original) The integrated circuit of claim 19, wherein, during operation, the column and the row adding is performed independently.